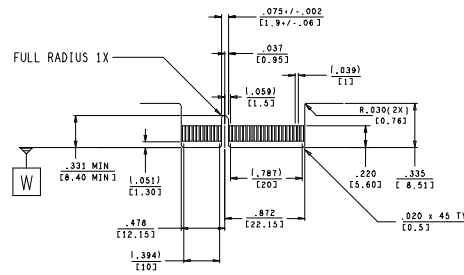


NOTES: UNLESS OTHERWISE SPECIFIED:

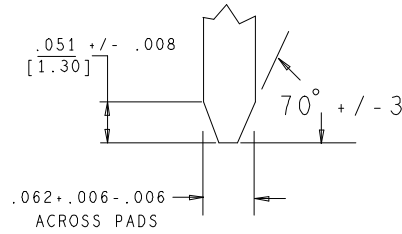
1. PART NUMBER IS PARENT ITEM IDENTIFICATION NUMBER ON ENGINEERING BILL OF MATERIAL.
2. THIS DRAWING ALSO EXISTS ON ELECTRONIC MEDIA.
3. VENDOR TO USE BLUE SOLDERMASK UNDER WHITE INK
- 4 THE CENTER OF THESE TWO HOLES ESTABLISH DATUM -A-.
- 5 DATUM -B- IS ESTABLISHED PERPENDICULAR TO DATUM -A- THROUGH THE CENTER OF THIS HOLE.
6. DIMENSIONING AND TOLERANCING PER AMSE Y14.5
- 7 FAB VENDOR MAY ADD COPPER THIEVING AS NEEDED ON OUTER LAYERS
COPPER THIEVING NO CLOSER THAN 0.100" TO ANY COPPER FEATURE OR LAYER WINDOW.
- 8 THESE HOLES MAY BE PLATED SHUT OR SOLDER FILLED.
- 9 GOLD FINGERS PER DETAIL A. PLATE MINIMUM 20 MIRCRO-INCHES GOLD OVER MINIMUM 150 MICRO-INCHES OF NICKEL.
10. CONTROLLED IMPEDANCE 42.5 OHMS +/- 5% 7.25 MIL TRACE LAYERS 1 & 4.
- 11 MARK PANEL NUMBER IN APPROXIMATE AREA SHOWN. AVOID SILKSCREEN OVER ANY TRACES
- 12 SURFACE FINISH:IMMERSION SILVER.



9
10

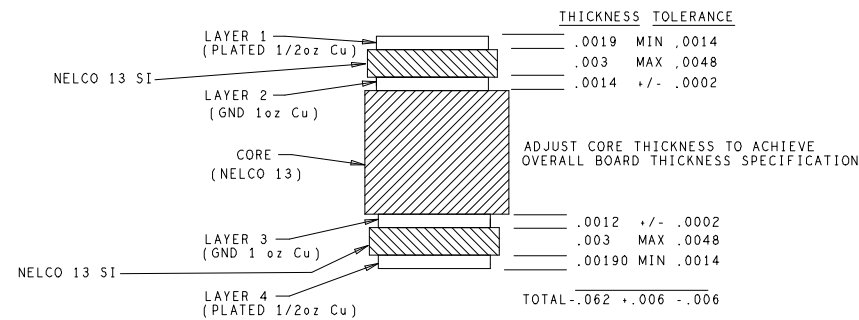
DETAIL - B

SCALE: NONE



VIEW B-B

SCALE: NONE



LAYER STACKING

SCALE: NONE

4 LAYER STACKUP